

App. Serial No. 10/550,340
Docket No.: NL030296

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In the Claims:

Please amend claims 1, 8 and 10 as indicated below. This listing of claims replaces all prior versions.

1. (Currently Amended) A transmitter comprising: a power amplifier (PA) having an amplifier power-supply input (PI) and an output (PAO) for supplying a transmission signal (Vo) with an output power (Po), a power supply (PS) having power supply outputs (PSO1, PSO2) for supplying a first power supply voltage (PV1) and a second power supply voltage (PV2), a switching circuit (SC) arranged between the power supply outputs (PSO1, PSO2) and the amplifier power-supply input (PI), and a controller (CO) having an input for receiving a power change command (PC) to control: (i) firstly, the switching circuit (SC) to supply the first power supply voltage (PV1) to the amplifier power-supply input (PI), and the power supply (PS) to vary a level of the second power supply voltage (PV2), the level of the second power supply voltage (PV2) being to be lower or higher than a level of the first power supply voltage (PV1) if the power change command (PC) indicates that the output power (Po) has to decrease and to be higher than the level of the first power supply voltage (PV1) if the power change command (PC) indicates that the output power (Po) has to increase, respectively, and (ii) secondly, the switching circuit (SC) to supply the second power supply voltage (PV2) to the amplifier power-supply input (PI).

2. (Original) A transmitter as claimed in claim 1, wherein the transmitter is a handheld apparatus (III) and further comprises a receiving circuit (RC) for receiving a power control signal (PCB) from a base station (BS) to supply the power change command (PC).

3. (Original) A transmitter as claimed in claim 2, wherein the transmitter is arranged for operation in a transmission system based on time slots (n-1, n, n+1) and a transition period (Tsw) during which the output power (Po) should be adapted, the transition period (Tsw) overlapping an end and/or a start of two successive time slots (n-1, n, n+1), respectively, and wherein the controller (CO) is adapted for controlling, firstly, the power